



SRM Valliammai Engineering College


(An Autonomous Institution)

SRM Nagar, Kattankulathur-603203, Chengalpattu District, Tamil Nadu

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Department of Electronics and Communication Engineering

Personal Details

Name :	SENTHILMURUGAN S		
Designation :	Assistant Professor (Sel. G)		
Educational Qualification :	B.E., M.E.,		
Experience:	15 Years 8 Months		
Area of Specialization :	VLSI Design		
Date of Joining :	26/05/2006		
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Educational Details

S.No.	Degree	Branch/Specialization	Institution / University	Year
1	B.E.	CSE	Jaya Engineering College/Madras University	1999
2	M.E.	VLSI Design	Arulmigu Kalasalingam College of Engineering/Anna University	2006
3	Ph.D.	Information and Communication	Anna University	Pursuing

Professional Society Memberships

1. Indian Society for Technical Education (ISTE) - Life Member: LM102358
2. Institution of Electronics & Telecommunication Engineers (IETE)- Life Member:M502530
3. Computer Society of India (CSI) - Life Member: I1505883
4. International Association of Engineers (IAENG)-Member:171769
5. Indian Science Congress Association: Life Member

Publication Details

Journals:

1. Priya Bharathi.K, Priya.T, Kiruthika.V, Senthilmurugan.S, "Smart Intrusion Detection System for Crop Protection and Monitoring", International Research Journal of Engineering and Technology (IRJET), Volume: 8, Issue: 03, Mar 2021.
2. Ayyadurai.B, Eugene.M, Haarish.U. P, Senthilmurugan.S, "Smart Transport System-Paving Way for Emergency Vehicles Using RSSI", International Journal of All Research Education and Scientific Methods (IJARESM), Volume 9, Issue 4, April -2021.
3. Sophia Varghees.V, Swathi.G, Vasanth Kumar.L, Senthilmurugan.S, "Non-Invasive Haemoglobin Measurement System Using IoT and Mobile Devices, International Journal of Innovative Research in Science, Engineering and Technology (IJIRSET), Volume 9, Issue 4, April 2020.
4. Santhosh Kumar.A, Vignesh.E, Vishnu Hara Sudan.J, Yogesh.K, Senthilmurugan. S, "FPGA based Visible Light Communication", International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol. 9, Issue 3, March 2020.

5. Rajkumar K, Saravanakumar C, Yuvashree C, Senthilmurugan S, "Portable Surveillance Robot Using IoT", International Research Journal of Engineering and Technology, vol.6, no.3, pp. 94-97, March 2019.
6. Praveenkumar S, Priyadarshiny B, Ramachandran M, Senthilmurugan S "Pollution Free Energy Generation from Speed Breaker", International Research Journal of Engineering and Technology, vol.6, no. 2, pp. 2214-2219, March 2019.
7. Anitha R, Baranilakshmi S, Hemalatha R, Anitha D, Senthilmurugan S, "Verilog HDL model of smart sensor for noise rejection in ECG signals", Journal of Innovation in Science and Engineering Research, vol. 2, no.1, pp. 225-264, March 2018.
8. Charan M, Gokulakrishnan G, Harikrishnan K, Harish A, Senthilmurugan S, "Real Time Vehicle Theft Prevention with Safety Measures using Arduino". International Journal of Advance Research in Science and Engineering, Vol. No. 07, Special Issue No.2, pp. 175-183, March 2018.
9. Praisylathna.G, Senthilmurugan S, "Efficient and Secured Data Transmission Disruption Tolerant Opportunistic Network", International Journal of Emerging Technology in Computer Science and Electronics. ISSN: 0976-1353 Volume 22 Issue 2, pp.226-230, MAY 2016.

Conferences:

1. Sreenath T, Tamilselvan A, Sunitha S, Shashanya M, Senthilmurugan S, "Civilian Communication System during Exigency using Software Defined Radio", National Conference on Electronics, Communication and Computing(NCECC-2017).
2. S.Aarthi S, Anand K, Elakkiya P, Akkshay Kiron P.A, Senthilmurugan S, "Analysis of Ethical Treatment in Animals Health Monitoring System" International Conference on Green, Intelligent Computing and Communication Systems(ICGICCS-2017).
3. Praisylathna.G, Senthilmurugan S, "Retrieval of Secured Data for Decentralized Disruption Tolerant Network in Military Environment", IEEE International Conference on Information Communication & Embedded System ICICES 2016.
4. Praisylathna.G, Senthilmurugan S, "Efficient and Secured Data Transmission Disruption Tolerant Opportunistic Network", International Conference on Advances in Emerging Technology(ICAET-2016).
5. Shobana M, Senthilmurugan S, "Reconfigurable Data processing using duplex fault tolerance system", International Conference on Innovations in Information, Embedded and Communication Systems (ICIIECS-2015), IEEE, Vol.1, PP.31-35.
6. Banumathy G, Senthilmurugan S, "MIMO Optical Wireless Communication Systems Employing an Efficient Precoder Matrix based Modulation", International Conference on Computational systems in Engineering and Technology, IEEE-978-1-4799.

Research and Development Details

1. No. of Research Projects Completed: Nil
2. No. of Patents Filed: Nil
3. No. of Ph.D. candidates guided/guiding: Nil
4. No of Conferences Convened: 1
Coordinated 2nd National Conference on “Microwave & Millimeterwave Communication (NCMMC-2015)” on 25th April 2015.

Other Particulars

1. No. of Books Published: Nil

Guest Lectures Delivered and Programmes Organized

1. Coordinator for Entrepreneurship Development Institute of India Sponsored Three Days “Entrepreneurship Awareness Camp”. Received a fund of Rs.20,000/- under DST-NIMAT Project for the Financial Year 2019-20. From 9.3.2020 to 11.03.2020
2. Coordinator for Entrepreneurship Development Institute of India Sponsored Three Days “Entrepreneurship Awareness Camp”. Received a fund of Rs.20,000/- under DST-NIMAT Project for the Financial year 2018-19. From 17.09.2018 to 19.09.2018
3. Coordinator for Entrepreneurship Development Institute of India Sponsored Three Days “Entrepreneurship Awareness Camp”. Received a fund of Rs.20,000/- under DST-NIMAT Project for the Financial year 2018-19. From 26.09.2017 to 28.09.2017
4. Delivered a session on AICTE Sponsored Six days Short Term Training Programme (STTP) on Modern Optimization Tools in Engineering Field-V organized by Department of EIE, SRM VEC, December 2020.
5. Delivered a session on AICTE Sponsored Six days Short Term Training Programme (STTP) titled “A Hands-on approach for Design and Implementation of Digital Circuits using Open-Source Computer Aided Design (CAD) tools” from 23.11.2020 to 28.11.2020.
6. Delivered a Lecture in the Value-Added Course on “Hardware Description Language (HDL) Programming using EDA tools” from 24th July 2017 to 11th August 2017 at VEC.
7. Conducted a Workshop on PCB Designing, Organized by Department of ECE on 12th August 2016, at VEC.
8. Delivered a Lecture on EC6601-VLSI Design in the Two-Day Faculty Development Programme, organized by Department of ECE on 27.01.2016 and 28.01.2016 at VEC.
9. Conducted two days training Programme on “Mentor Graphics IC Nanometer Design Tools” on 6th and 7th May 2015 at VEC.
10. Organized Skill Development Programme on “Hands on Training for Laboratory Equipment’s and Software Tools” for programmers and Lab Assistants on 3rd and 4th December 2014 at VEC.
11. Organized VLSI Training Programme on “Mentor Graphics and Xilinx Tools” from 12th to 14th July 2011 at VEC.