SRM VALLIAMMAI ENGINEERING COLLEGE

(An Autonomous Institution)

SRM Nagar, Kattankulathur – 603 203

DEPARTMENT OF INFORMATION TECHNOLOGY

QUESTION BANK



III SEMESTER- SECOND YEAR

1908303- ANALOG AND DIGITAL COMMUNICATION

Regulation – 2019

Academic Year: 2022 – 2023 ODD

Prepared by

Mr.R.IssanRaj, Assistant.Professor(Sr.G) / EIE

Mr.N.Sowrirajan, Assistant.Professor(O.G) / EIE

SRM VALLIAMMAI ENGINEERING COLLEGE



SRM Nagar, Kattankulathur-603203



DEPARTMENT OF INFORMATION TECHNOLOGY

QUESTION BANK

SUBJECT : 1908303 - ANALOG AND DIGITAL COMMUNICATION

SEM / YEAR : III SEMESTER/ SECOND YEAR

UNIT -I ANALOG COMMUNICATION

Introduction to Communication Systems - Modulation – Types - Need for Modulation. Theory of Amplitude Modulation - Theory of Frequency and Phase Modulation – Comparison of Analog Communication Systems (AM -FM-PM).

CINGI PART - A						
Q. No	Questions	Competence	BT Level			
1.	What is the need for modulation?	Remembering	BTL1			
2.	Define amplitude modulation.	Remembering	BTL1			
3.	Define modulation index.(OR) Define the Degree of modulation in AM	Remembering	BTL1			
4.	Consider an AM signal $x(t)=2\cos(2\pi f_c t) +0.5\cos(2\pi f_c t).\cos(2\pi f_m t)$. Find the	Evaluating	BTL5			
	modulation index used to generate the signal.					
5.	Plan the bandwidth which is needed to transmit voice signal of 4kHz, use AM.	Creating	BTL6			
6.	The output voltage of a transmitter is given by $500(1+0.4\sin 3140t) \sin 6.28 \times 10^7 t$.	Applying	BTL3			
	Find the carrier frequency and modulating frequency.					
7.	In an amplitude modulation system, the carrier frequency is $f_c=100kHz$. The	Applying	BTL3			
	maximum frequency of the signal is 5 kHz. Calculate the lower & upper side					
	bands and bandwidth of the AM signal.					
8.	A carrier of 10MHz frequency and peak value of 10 V is amplitude modulated	Evaluating	BTL5			
	by a 5 KHz sine wave of 6 V amplitude. Find the modulation index.					
9.	Define amplitude and angle modulation.	Understanding	BTL2			
10.	Write down the mathematical expression for angle modulated wave.	Remembering	BTL1			
11.	Plot the FM waveform for the given message using a sine wave carrier.	Understanding	BTL2			

	x(t)	\sim \sim .			
	0				
	•				
12.	Diffe	rentiate between narrow band and wide band FM signal.		Analysing	BTL4
13.	Drav	v the phasor diagram of narrow band FM.		Applying	BTL3
14.	Wha	t is the relationship between PM and FM?		Analysing	BTL4
15.	Find	the modulating frequency and maximum deviation of the PM	wave	Applying	BTL3
	repre	sented by $v(t) = 12\sin(6x10^8t + 5\cos 1250t)$.			
16.	Desig	gn the bandwidth of FM signal if the frequency deviation of the modu	lator	Creating	BTL6
	is 25	kHz per Volt?			
17.	Disti	nguish between frequency and phase modulation.		Analysing	BTL4
18.	State	the advantages of FM modulation.		Understanding	BTL2
19.	Desc	ribe Carson's rule.		Understanding	BTL2
20.	Drav	v the Schematic of generating FM signal using Phase Modulator.		Remembering	BTL1
21.	Wha	t is the purpose of limiter in FM receiver?		Understanding	BTL2
22.	The 1	naximum frequency deviation in an FM is 10kHz and the signal frequ	Evaluating	BTL5	
	is 10kHz. Estimate the bandwidth using Carson's rule and the modulation				
	index				
23.	How	the pre-emphasis and de-emphasis circuit improve the performance of	FM?	Remembering	BTL1
24.	In an	FM wave, frequency deviation is 25 kHz. What maximum phase devi	ation	Analysing	BTL4
	does	this represent if the modulating signal has 100Hz frequency? PART – B			
Q. No		Questions		Competence	BT
1.	With	the help of mathematical expression explain about amplitude	13	Understanding	BTL2
	modu	alation, its generation and detection.		,	
2.	(i)	Develop the expression for instantaneous voltage of AM wave.	06	Applying	BTL3
	(ii)	In modulation by several sine waves simultaneously, in AM, the bandwidth required is twice the highest modulating frequency	07		
		Prove this concept using appropriate expression.			
3.	(i)	The output modulated wave of a standard AM transmitter is	02	Analysing	BTL4
		represented $S(t) = 500(1+0.4\sin 3140t)\cos(6.28x10')t$. This Voltage	02		
		is fed to a load of 60002. Analyse the following (a) Modulating Erequency (b) Carrier Erequency (c) Mean power output	02		
	(ii)	Derive efficiency $\acute{\eta}$ of standard AM and show that for a single tone	07		
		AM, ή _{max} =33.3% at m=1.			
4.	An a	udio frequency signal 10 sin $(2 \times 3.14 \times 500)$ t is used to amplitude		Analysing	BTL4
	modu	solution index $(5 \times 3.14 \times 10^3)$ t. Calculate and Analyse	02		
	(1)		02		

(ii) Upper and lower side band frequencies			
	03		
nvelope	03		
	03		
B-SC system in terms of BW,	07	Applying	BTL3
gram and efficiency.			
ated using envelope detector	06		
constant.			
carrier with peak amplitude		Applying	BTL3
nodulating signal that is of			
the output wave of ± 7.5 V.			
	02		
er, the upper and lower side	02		
	04		
AM envelope	02		
3.	03		
a naasiyyan with maat blaaly	12	I Indonation din a	
EER,	15	Understanding	DILZ
when the carrier and one of		Evoluting	BTI 5
when the carrier and one of		Evaluating	DILJ
	02		
<u>n</u>	02		
here modulation and their	02		
r phase modulation and then	07		
er power in AM in terms of	06	Remembering	BTL1
or an angle modulated wave	07		
d FM and explain about the	07	Remembering	BTL1
modulation and frequency	06		
1.001.1.0	0.5		
r power and efficiency of	06	Evaluating	BTL5
or FM system using Ressel	07		
of The system using Desser	07		
ect method and write its	13	Understanding	BTL2
		C	
direct FM transmitter and	13	Remembering	BTL1
		C	
arrier signal frequency of		Creating	BTL6
A modulating frequency of		U U	
uency deviation of 10 kHz.	03		
	nvelope B-SC system in terms of BW, gram and efficiency. Ited using envelope detector constant. carrier with peak amplitude nodulating signal that is of n the output wave of ±7.5 V. I.er, the upper and lower side e. AM envelope e. e receiver with neat block when the carrier and one of wave modulated to a depth I phase modulated to a depth I phase modulated wave d FM and explain about the e modulation and frequency or power and efficiency of or FM system using Bessel ect method and write its ndirect FM transmitter and arrier signal frequency of uency deviation of 10 kHz.	nvelope02 03 03nvelope03 03ost03 03ost03 03servelope06 constant.carrier with peak amplitude nodulating signal that is of n the output wave of ±7.5 V.er, the upper and lower side e.02 02 04 02 03er, the upper and lower side e.02 02 04 02 03er receiver with neat block13when the carrier and one of wave modulated to a depth 02 0202 02l phase modulation and their 0909er power in AM in terms of 0606or an angle modulated wave 0707d FM and explain about the of or FM system using Bessel of or FM system using Bessel of or FM system using Bessel of A modulating frequency of A modulating frequency of puency deviation of 10 kHz.03	nvelope02 03 03nvelope03 03o-SC system in terms of BW, gram and efficiency. tted using envelope detector06constant.06carrier with peak amplitude nodulating signal that is of n the output wave of ±7.5 V.Applyinger, the upper and lower side e.02 02 04er, the upper and lower side e.02 02 03e receiver with neat block13Understandingwhen the carrier and one of wave modulated to a depth 02 02Evaluatingor an angle modulated wave or an angle modulated wave07d FM and explain about the or FM system using Bessel07ert method and write its adirect FM transmitter and arrier signal frequency of a. A modulating frequency of puency deviation of 10 kHz.Creating

	(••)		00		
	(11)	A 25 MHz carrier is modulated by a 400 Hz audio sine wave. If the	03		
		carrier voltage is 4v and the maximum frequency deviation is	02		
		10kHz and phase deviation is 25 radians. Write the equation of this	02		
		modulated wave for (1) FM (11) PM. If the modulating frequency is			
		now changed to 2kHz, all else remaining constant. Write a new			
1 =	D '		10	D	
15.	Discu	uss about demodulators which demodulate the FM wave.	13	Remembering	BTLI
16.	(i)	Compare wide band and narrow band FM system.	06	Understanding	BTL2
	(ii)	Explain the detection of FM using PLL detector.	07		
17.	Com	pare AM, FM and PM.	13	Analysing	BTL4
		PART – C			1
Q. No		Questions		Competence	BT
1	A A	\mathbf{M} since \mathbf{I} has a model to model and the last \mathbf{I} and \mathbf{I}		Encelar et in e	Level
1.	An A	IN signal has a peak to peak unmodulated carrier voltage is 200V,		Evaluating	BILD
	Voice	signal range is 300Hz to 3400Hz and a load resistance is 5002. The			
	mess	age is modulated critically. Determine the following	02		
	1) Ca	mer of Side hands	02		
	2) P0	wel of Side Dalius	02		
	$\frac{3}{1}$	aw the Spectrum of above spectrication with an components	02		
	(4)100 5)Tro	ar power for Ann	02		
	6)Sel	ect the suitable AM modulation technique for defence and	03		
	ent	ertainment application and justify your answers	05		
2	(i)	For an AM DSBEC transmitter with an unmodulated carrier power	08	Evaluating	BTI 5
2.	(1)	$P_c=100$ W that is modulated simultaneously by three modulating	00	Livalaating	DILS
		signals with coefficients of modulation $m_1 = 0.2$, $m_2 = 0.4$, $m_3 = 0.5$			
		determine:			
		1)Total coefficient of modulation			
		2)Upper and lower sideband power			
		3)Total transmitted power			
	(ii)	A 400 W carrier is amplitude modulated to a depth of 100%.	07	Evaluating	BTL5
		Calculate the total power in case of the AM and DSBSC techniques.		C C	
		How much power saving in watts is achieved for DSBSC? If the			
		depth of modulation is changed to 75%, then how much power in			
		W is required for transmitting the DSBSC wave? Compare the			
		power required for DSBSC in both cases and comment on the			
		reason for change in the power levels.			
3.	Desig	gn a FM broadcasting station by identifying the blocks involved in	15	Creating	BTL6
	that.	Discuss the generation, theory involved in the WBFM and NBFM.			
4.	(i)	A FM radio link has a frequency deviation of 30kHz.The	03	Creating	BTL6
		modulating frequency is 3kHz. Find the bandwidth needed for the		6	_
		link.			
	(ii)	An angle modulated signal has the form			
	<u> </u>	$v(t)=100 \cos[2\pi f_c t+4\sin 2000\pi t]$ where $f_c=10MHz$			
		Find : (a) The Average transmitted power	02		
		(h) Peak phase deviation	02		
		(c) Peak frequency deviation	03		
		(d) Is this FM or a PM signal? Explain	03		
			04		

5.	(i)	Consider an angle modulated signal $x(t) = 3\cos[2\pi \times 10^6 t + 2\sin(2\pi \times 10^6 t)]$	09	Evaluating	BTL5
		$(2\pi \times 10^{3} t)$]. Find its			
		(1) Instantaneous frequency at time t=0.25 ms and t=0.5 ms			
		(2) Maximum phase deviation			
		(3) Maximum frequency deviation			
	(ii)	Determine the peak frequency deviation and modulation index (m)	06		
		for an FM modulator with a deviation sensitivity $K_1=5 \text{ kHz/V}$ and			
		a modulating signal V(t)= $2\cos(2\pi \times 2000t)$.			

UNIT -II PULSE COMMUNICATION

Pulse Communication: Pulse Amplitude Modulation (PAM) – Pulse Time Modulation (PTM) – Pulse code Modulation (PCM) - Comparison of various Pulse Communication System (PAM – PTM – PCM).

PART – A					
Q. No	Questions	Competence	BT Level		
1.	What is digital pulse modulation?	Understanding	BTL2		
2.	State the sampling theorem for band limited signal of finite energy.	Applying	BTL3		
3.	Differentiate natural and flat top sampling	Analysing	BTL4		
4.	Infer about Quantization process.	Evaluating	BTL5		
5.	What is quantization error?	Remembering	BTL1		
6.	Define Nyquist theorem.	Remembering	BTL1		
7.	Prepare the Nyquist rate for analog input frequency of a) 4kHz b) 10kHz.	Creating	BTL6		
8.	Define Aliasing and Aperture effect.	Remembering	BTL1		
9.	For the signal m (t) = $3 \cos 500 * 3.14t + 4 \sin 1000* 3.14 t$, Determine the	Evaluating	BTL5		
	Nyquist sampling rate.				
10.	Infer about PAM.	Understanding	BTL2		
11.	Define quantization error?	Understanding	BTL2		
12.	Why do we encounter aperture effect in PAM? How will you rectify it?	Analysing	BTL4		
13.	Develop the PWM and PPM waveforms.	Creating	BTL6		
14.	Identify the steps in pulse code modulation	Remembering	BTL1		
15.	In PCM, why flat top sampling is preferred over natural sampling?	Analysing	BTL4		
16.	How many bits are required to represent a sample in a PCM system with 32	Applying	BTL3		
	quantisation levels?				
17.	Illustrate the regenerative repeaters	Applying	BTL3		
18.	What is the necessity of companding?	Remembering	BTL1		
19.	Illustrate about companding	Applying	BTL3		
20.	Define overload distortion.	Understanding	BTL2		
21.	What is nonuniform or nonlinear encoding?	Understanding	BTL2		

22.	List the advantages of PCM.		Remembering	BTL1
23.	Mention how PPM is derived from PWM		Analysing	BTL4
24.	Interpret the advantages of PWM?		Evaluating	BTL5
	PART – B			
Q. No	Questions		Competence	BT Level
1.	Explain in detail the Nyquist criterion for distortion less transmission of baseband PAM signal	13	Applying	BTL3
2	Discuss about the generation and demodulation of PAM signal with	13	Understanding	BTI 2
2.	necessary waveforms	15	Onderstanding	DILZ
3.	Explain quantization process in detail and derive the expression for output	13	Analysing	BTL4
	signal to noise ratio of uniform quantizer.	10	·	
4.	(i) What are the types of sampling? Explain the operation of the sample and hold circuit.	07	Understanding	BTL2
	(ii) Describe the generation and demodulation of PPM signal with necessary waveforms.	06		
5.	Construct a digital transmission system with suitable diagrams for the	13	Remembering	BTL1
	following conditions 1.Output as samples with constant width and			
	constant amplitude 2. Output as samples with variable width and constant			
	amplitude. Choose the best method for your own application and justify.	10		
6.	Explain about various operations performed in the transmitter and	13	Applying	BTL3
7	What is companding? Explain in detail Analog and digital companding	13	Pamambaring	BTI 1
7.	what is companding? Explain in detail Analog and digital companding.	15	Kennennbernig	DILI
8.	Compare the various Pulse modulation techniques	13	Analysing	BTL4
9.	(i) A Composite video signal with base band frequency range from	03	Evaluating	BTL5
	zero to 4MHz is transmitted by linear PCM, using 8 bit per sample			
	and sampling rate of 10 MHz i) Determine the number of			
	quantization level ii) Calculate the transmission bit rate iii) What is			
	the type of noise introduced in this process. (ii) An audia signal $S(t) = 2 \cos(2t/2) 4t/(500 t)$ is guartized using 10 hit	10		
	(ii) An audio signal, $S(t) = 3 \cos(2 \times 3.14 \times 500 t)$ is quantized using 100it PCM. Determine the signal to quantization noise ratio.	10		
10.	Explain the generation of PCM signal with a block diagram.	13	Remembering	BTL1
11.	Analyse a PCM system with quantization level 8 in the dynamic range of	13	Analysing	BTL4
	2V. Determine the quantization error and mean square error for the			
	measured consecutive samples of 1.2V, 1.0V, 0.95V, 1.41V and 1.65V			
10	readings.			
12.	For a PCM system with the following parameters, determine (i) Minimum sampling rate	02	Evaluating	BTL5
	(ii) Minimum number of bits used in the PCM code	03		
	(iii) Resolution and	03		
	(iv) Quantization error	04		
	Maximum analog input frequency = 4 KHz	~ •		
	Minimum dynamic range = 46 dB .			

13.	Desig	gn a PCM system with suitable blocks with the maximum number of	13	Creating	BTL6
	bits p	ber sample, minimum sampling rate and bit transmission rate for the			
	follo	wing parameters. Information is in an analog waveform with			
	maxi	mum frequency 3 kHz and the number of pulse level is $M=16$.			
14.	Wha	t is DPCM? Explain its principle with neat block diagram.	13	Understanding	BTL2
15.	(i)	Derive the expression for signal to noise ratio in a 3 bit PCM	07	Understanding	BTL2
		coding.			
	(ii)	With suitable block diagram explain the operation of digitally companded PCM system.	06		
16.	(i)	Draw the block diagram for simplex PCM transmission system and	08	Remembering	BTL1
	<i>(</i> 11)	explain its operation.	07		
1.	(ii) (i)	Explain the working of differential pulse code modulation.	05	A 1 '	
17.	(1)	Draw the schematic of PCM and explain the sampling and	10	Applying	BTL3
	(ii)	A PCM scheme transmits the signal at a rate 64 kbps. If it uses 8	03		
	(11)	hits/sample calculate the sampling rate and minimum frequency	05		
		that can be present in its input to reconstruct the same without any			
		error.			
		PART – C	•		
Q.N		Questions		Competence	BT
0		MALL WG	1		Level
1.	(i)	A base band signal having maximum frequency of 30 kHz is required to be transmitted using a digital audio system with a	03	Evaluating	BTL5
	sampling frequency of 44.1 kHz, Estimate the frequency				
		components available at the output			
	(;;)	How aliasing affasts can be avanaged	06		
	(ii)	How aliasing effects can be overcome.	06		
	(ii) (iii)	How aliasing effects can be overcome. How is the PCM different from PAM?	06 06		
2.	(ii) (iii) Expl	How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver.	06 06 15	Creating	BTL6
2. 3.	(ii) (iii) Expl Desc	How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram.	06 06 15 15	Creating Evaluating	BTL6 BTL5
2. 3. 4.	(ii) (iii) Expl Desc Illus	How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail.	06 06 15 15 15	Creating Evaluating Creating	BTL6 BTL5 BTL6
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i	How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted	06 06 15 15 15	Creating Evaluating Creating Evaluating	BTL6 BTL5 BTL6 BTL5
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i over	How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1%(full scale).The analog	06 06 15 15 15	Creating Evaluating Creating Evaluating	BTL6 BTL5 BTL6 BTL5
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i over volta	How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1%(full scale).The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of a + 10 volts. Determine	06 06 15 15 15	Creating Evaluating Creating Evaluating	BTL6 BTL5 BTL6 BTL5
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i over volta -10 to	How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of $\pm 0.1\%$ (full scale).The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of p + 10 volts. Determine The maximum sampling rate required	06 06 15 15 15 03	Creating Evaluating Creating Evaluating	BTL6 BTL5 BTL6 BTL5
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i over volta -10 to (i)	 How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1%(full scale). The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of p+10 volts. Determine) The maximum sampling rate required. i) The number of bits in each PCM word. 	06 06 15 15 15 03 04	Creating Evaluating Creating Evaluating	BTL6 BTL5 BTL6 BTL5
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i over volta -10 to (i (i)	 How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1%(full scale). The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of p+10 volts. Determine) The maximum sampling rate required. i) The number of bits in each PCM word. ii) The minimum bit required in the PCM signal. 	06 06 15 15 15 03 04 04	Creating Evaluating Creating Evaluating	BTL6 BTL5 BTL6 BTL5
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i over volta -10 to (i) (i)	 How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1%(full scale). The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of p+10 volts. Determine) The maximum sampling rate required. i) The number of bits in each PCM word. ii) The minimum bit required in the PCM signal. v) The minimum absolute channel bandwidth required for the 	06 06 15 15 15 03 04 04 04	Creating Evaluating Creating Evaluating	BTL6 BTL5 BTL6 BTL5
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i over volta -10 to (i (i) (i)	 How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1%(full scale). The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of p+10 volts. Determine) The maximum sampling rate required. i) The number of bits in each PCM word. ii) The minimum bit required in the PCM signal. v) The minimum absolute channel bandwidth required for the transmission of the PCM signal. 	06 06 15 15 15 03 04 04 04 04	Creating Evaluating Creating Evaluating	BTL6 BTL5 BTL6 BTL5
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i over volta -10 to (i (i) (i)	 How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1%(full scale). The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of p+10 volts. Determine) The maximum sampling rate required. i) The number of bits in each PCM word. ii) The minimum bit required in the PCM signal. v) The minimum absolute channel bandwidth required for the transmission of the PCM signal. UNIT –III DIGITAL COMMUNICA 	06 06 15 15 15 03 04 04 04 04	Creating Evaluating Creating Evaluating	BTL6 BTL5 BTL6 BTL5
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i over volta -10 tc (i (i) (i) (i) litude	 How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1%(full scale). The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of p+10 volts. Determine The maximum sampling rate required. The number of bits in each PCM word. The minimum bit required in the PCM signal. V) The minimum absolute channel bandwidth required for the transmission of the PCM signal. UNIT –III DIGITAL COMMUNICA Shift Keying (ASK) – Frequency Shift Keying (FSK)–Phase Shift 	06 06 15 15 15 03 04 04 04 04 04 t Keyi	Creating Evaluating Creating Evaluating N N ng (PSK) – BPSK	BTL6 BTL5 BTL6 BTL5
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i over volta -10 to (i (i) (i) (i) Exco	 How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1%(full scale). The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of p+10 volts. Determine) The maximum sampling rate required. i) The number of bits in each PCM word. ii) The minimum bit required in the PCM signal. v) The minimum absolute channel bandwidth required for the transmission of the PCM signal. UNIT –III DIGITAL COMMUNICA Shift Keying (ASK) – Frequency Shift Keying (FSK)–Phase Shift 	06 06 15 15 15 03 04 04 04 04 04 04 TIO t Keyi – PSI	Creating Evaluating Creating Evaluating N ng (PSK) – BPSK K).	BTL6 BTL6 BTL5
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i over volta -10 to (i) (i) (i) (i) (i) Bitude	How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1%(full scale). The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of p+10 volts. Determine) The maximum sampling rate required. ii) The number of bits in each PCM word. iii) The minimum bit required in the PCM signal. v) The minimum absolute channel bandwidth required for the transmission of the PCM signal. UNIT –III DIGITAL COMMUNICA Shift Keying (ASK) – Frequency Shift Keying (FSK)–Phase Shift mparison of various Digital Communication System (ASK – FSK PART A	06 06 15 15 15 03 04 04 04 04 04 04 TIO t Keyi – PSI	Creating Evaluating Creating Evaluating N N ng (PSK) – BPSK X).	BTL6 BTL5 BTL6 BTL5
2. 3. 4. 5. 5. Amp - 8PS Q. N0	(ii) (iii) Expl Desc Illus The i over volta -10 td (i (i) (i) (i) (i) (i) (i) (i) (i) (i)	How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1%(full scale). The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of o +10 volts. Determine) The maximum sampling rate required. ii) The number of bits in each PCM word. iii) The minimum bit required in the PCM signal. v) The minimum absolute channel bandwidth required for the transmission of the PCM signal. UNIT –III DIGITAL COMMUNICA Shift Keying (ASK) – Frequency Shift Keying (FSK)–Phase Shift Mart A	06 06 15 15 15 03 04 04 04 04 04 TIO t Keyi – PSI	Creating Evaluating Creating Evaluating N ng (PSK) – BPSK K). Competence	BTL6 BTL5 BTL5 BTL5
2. 3. 4. 5.	(ii) (iii) Expl Desc Illus The i over volta -10 to (i) (i) (i) (i) SK.Co	 How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1%(full scale). The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of 0 +10 volts. Determine) The maximum sampling rate required. i) The number of bits in each PCM word. ii) The minimum bit required in the PCM signal. v) The minimum absolute channel bandwidth required for the transmission of the PCM signal. UNIT –III DIGITAL COMMUNICA Shift Keying (ASK) – Frequency Shift Keying (FSK)–Phase Shift mparison of various Digital Communication System (ASK – FSK) 	06 06 15 15 15 03 04 04 04 04 04 04 TIO t Keyi – PSI	Creating Evaluating Creating Evaluating N N ng (PSK) – BPSK K).	BTL6 BTL5 BTL5 BTL5
2. 3. 4. 5. 5. Amp - 8PS Q. N0	(ii) (iii) Expl Desc Illus The i over volta -10 to (i (i) (i) (i) (i) (i) (i) (i) (i) (i)	How aliasing effects can be overcome. How is the PCM different from PAM? ain in detail the Delta modulation transmitter and Receiver. ribe the operation of DPCM system with a relevant diagram. trate the concepts of PWM in detail. nformation in an analog signal voltage waveform is to be transmitted a PCM system with an accuracy of ±0.1% (full scale). The analog ge waveform has a bandwidth of 100 Hz and an amplitude range of b +10 volts. Determine) The maximum sampling rate required. i) The number of bits in each PCM word. ii) The minimum bit required in the PCM signal. v) The minimum absolute channel bandwidth required for the transmission of the PCM signal. UNIT –III DIGITAL COMMUNICA Shift Keying (ASK) – Frequency Shift Keying (FSK)–Phase Shift mparison of various Digital Communication System (ASK – FSK PART A	06 06 15 15 15 03 04 04 04 04 04 TIO t Keyi – PSI	Creating Evaluating Creating Evaluating N ng (PSK) – BPSK X).	BTL6 BTL5 BTL5 BTL5 C-QPSK BT Level

2.	State	Baud rate and Bit rate.	Remembering	BTL1	
3.	Defir	e Bandwidth efficiency		Remembering	BTL1
4.	Sket	\mathbf{h} the digitally modulated waveforms for the binary data 1011001 usi	ng	Creating	BTL6
	ASK	and FSK.			
5.	Defir	e peak frequency deviation for FSK.		Understanding	BTL2
6.	Writ	e down the expression for peak frequency deviation of FSK.		Applying	BTL3
7.	Dete	rmine the peak frequency deviation for a binary FSK signal with a	mask	Evaluating	BTL5
	frequ	ency of 49 kHz, a space frequency of 51 kHz.			
8.	Why	is FSK and PSK signals are preferred over ASK signals?		Analysing	BTL4
9.	Desc	ribe the constellation diagram of ASK, FSK signal.		Understanding	BTL2
10.	Drav	the FSK and PSK waveforms of the bit stream 101.		Remembering	BTL1
11.	Drav	the constellation diagram of PSK, QPSK signal.		Applying	BTL3
12.	For a	n 8PSK system, the operating with an information bit rate of 24 l	kbps,	Evaluating	BTL5
	deter	mine bandwidth efficiency.			
13.	For 1	6 PSK and a transmission system with a 10kHZ bandwidth. Fine	d the	Evaluating	BTL5
14	maxi Drav	mum bit rate.		Applying	BTI 3
17.					
15.	Sket	ch the QPSK signal for the binary sequence 11001100.		Creating	BTL6
16.	Com	pare the average power requirements of QPSK and BPSK.		Analysing	BTL4
17.	Diffe	rentiate between BPSK from QPSK.		Analysing	BTL4
18.	For tl	ne given the input binary sequence 100100010, sketch the waveform of	of the	Applying	BTL3
	in ph	ase and quadrature components of a modulated wave obtained by u	ısing		
	QPSI	Κ.			
19.	What	do you meant by I,Q& C Channels?		Remembering	BTL1
20.	Disti	nguish between standard FSK and MSK.		Analysing	BTL4
21.	What	is DPSK?		Understanding	BTL2
22.	Wha	t is M-ary encoding?		Understanding	BTL2
23.	Give	any two applications of DPSK.		Understanding	BTL2
24.	Wha	t is Eye-pattern?		Remembering	BTL1
		PART B		Competence	рт
Q. NO		Questions		Competence	Level
1.	(i)	Explain the working principle of ASK with block diagram	10	Remembering	BTL1
	(ii)	Calculate Baud rate and Bit rate for ASK	03		

2.	Desc neces	ribe the generation and detection of binary FSK signal with sarry diagram and equation.	13	Understanding	BTL2
3.	Drav coher	v and explain the operations of FSK modulator and explain about rent and Non-coherent demodulators.	13	Remembering	BTL1
4.	A da Anal trans	ta bit sequence consists of the following string of bits 10 11 10 10. yze and draw the nature of waveform transmitted by BPSK mitter.	13	Understanding	BTL2
5.	(i) (ii)	Determine the following: (i) Peak frequency (ii) Minimum bandwidth (iii) Baud, for FSK signal with a mark frequency of 49 kHz, space frequency of 51kHz, and input bit rate of 2 kbps. Explain about DPSK.	03	Applying	BTL3
6	(i)	Write short notes on the spectrum and handwidth of FSK	08	Understanding	BTI 2
	(i) (ii)	For a BPSK modulator with a Carrier frequency of 70 MHz and an input bit rate of 10 Mbps, determine the maximum and minimum upper and lower side frequencies, draw the output spectrum, determine the minimum Nyquist bandwidth, and calculate the baud.	02 01 01 01		DILL
7.	(i) (ii) (iii)	Explain the working of BPSK transmitter and receiver with necessary equation and block diagram.Differentiate coherent and non-Coherent detectionCompare the various digital communication systems.	06 02 05	Analysing	BTL4
8.	For a bit ra lower Nyqu	BPSK modulator with a Carrier frequency of 70 MHz and an input ite of 10 Mbps, determine the maximum and minimum upper and r side frequencies, draw the output spectrum, determine the minimum ist bandwidth, and calculate the baud (Assume $f= 5MHz$)	13	Applying	BTL3
9.	Expl with	ain the generation and reception of binary phase shift keying signal necessary block diagram.	13	Evaluating	BTL5
10.	Drav modu	v the constellation diagram of QPSK modulation and explain QPSK lation and QPSK demodulation.	13	Remembering	BTL1
11.	(i) (ii)	Draw the ASK, FSK and BPSK wave forms for the bit stream 10110001 For QPSK modulator with an input data rate equal to 12 Mbps and a carrier frequency of 100 MHz. Determine the following. i).Minimum double sided Nyquist bandwidth ii).Baud Rate and	06 03 02	Applying	BTL3
		iii). Sketch the output spectrum	02		
12.	(i) (ii)	For a QPSK modulator with an input data rate equal to 10Mbps and a carrier frequency of 70MHz, find the minimum double sided Nyquist bandwidth and baud. Compare OPSK and BPSK.	07	Analysing	BTL4
13.	Desi	gn bandwidth efficiency of M-ary PSK and compare it with other M-SK schemes.	13	Creating	BTL6
14.	Drav 1001 Note	w the QPSK and 8-PSK wave forms for the bit stream 110001010101 I f needed discard the bits to a minimum extend.	13	Remembering	BTL1

15.	(i)	Explain the relationship between the minimum bandwidth	07	Understanding	BTL2		
		required for a QPSK system and the bit rate.		C C			
	(ii)	Summarize about the operation of 8 PSK system.	06	•			
16.	(i)	If a digital message input data rate is 8 kbps and average energy per	06	Evaluating	BTL5		
		bit is 0.01 units. Find the bandwidth required for transmission of		-			
		the message through BFSK, BPSK, and QPSK.					
	(ii)	Determine the baud, minimum bandwidth and bandwidth	07	-			
		efficiency for an 8-PSK system operating with an information bit rate of 24kbps.					
17.	Com	pare and contrast the various Digital Communication systems.	13	Analysing	BTL4		
		PART – C					
Q. NO		Questions		Competence	BT Level		
1.	Drav	the block diagram of FSK receiver and explain the operation.	15	Evaluating	BTL5		
	Deter	mine the following :					
	(i) pe	ak frequency deviation					
	(ii) m	inimum bandwidth					
	(iii)	Baud for FSK signal with a mark frequency of 49 kHz, space					
2	trequ	ency of 51 kHz, and input bit rate of 2 kbps.	00	Creating			
۷.	(I)	List out its marits over DSK	08	Creating	DILO		
2	(II) W:4h	List out its ments over PSK,	15	Evolution	DTI 5		
э.	recei	ver and bandwidth consideration of QPSK.	15	Evaluating	BILS		
4.	(i)	In a digital CW communication system, the bit rate of a bipolar NRZ	10	Creating	BTL6		
		data sequence is 1 Mbps and carrier frequency is 100 MHz. find the					
		symbol rate of transmission and the bandwidth requirement of the					
		communications channel in the following cases of different					
		(i) BPSK system					
		(ii) OPSK system					
		(iii) 16-ary PSK system					
	(ii)	Compare the bit error performance for PSK, DPSK and FSK.	05				
5.	(i)	A data bit sequence consists of the following strings of bits	06	Evaluating	BTL5		
		10111010. Develop and draw the nature of wave form transmitted					
		by BPSK transmitter					
	(ii)	Illustrate the concept of generation and demodulation of PPM with	09				
		block diagram.					
		UNIT – IV DATA COMMUNICATIO	N				
Data	Comn	nunication: History of Data Communication - Standards Organization	ns for	Data Communica	tion- Data		
Com	munica	ation Circuits - Data Communication Codes - Data communication	n Hai	rdware - serial an	d parallel		
interf	aces.						
	PART –A						

Q. No	Ques	tions	Competence	BT Level	
1.	Nam	e the standards organizations for data communication?	Understanding	BTL2	
2.	List	out all data communication codes		Remembering	BTL1
3.	Disti	nguish between half duplex and full duplex transmission.		Analysing	BTL4
4.	Wha	t is data modem?		Understanding	BTL2
5.	List	out the layer presented in ISO-OSI reference model		Understanding	BTL2
6.	Inter	pret about USRT, USART.		Evaluating	BTL5
7.	Dete	rmine the odd and even parity bits for the ASCII character R whose I	Hex	Evaluating	BTL5
	code	is 52.			
8.	Defii	ne DTE, DCE.		Remembering	BTL1
9.	Inter	pret about any two function of UART.		Applying	BTL3
10.	Illus	trate about Synchronous serial data		Understanding	BTL2
11.	Illus	trate ESA.		Applying	BTL3
12.	Discu	uss about the responsibilities of IAB.		Creating	BTL6
13.	List	out the components of communication circuits.		Applying	BTL3
14.	Class	sify the types of transmission medium		Analysing	BTL4
15.	Give	the problems with Boudot code CNGINEER		Creating	BTL6
16.	5. Infer the ASCII code.			Understanding	BTL2
17.	Diffe	rentiate the synchronous and Asynchronous modems.	Analysing	BTL4	
18.	Writ	e the some commands for modem control.	Remembering	BTL1	
19.	Illus	trate about the error control		Applying	BTL3
20.	Defii	ne burst error.		Remembering	BTL1
21.	Poin	t out the redundancy check types.		Remembering	BTL1
22.	Class	sify the different types of data communication codes.		Analysing	BTL4
23.	Wha	t is meant by data communication equipment		Remembering	BTL1
24.	Dete	rmine the noise margins for an RS-232 interface with driver s	ignal	Evaluating	BTL5
	volta	ges of ±6 V			
		PART –B			
Q. No	Ques	tions		Competence	BT Level
1.	Expl stand	ain the data communication network architecture protocols and ards in details.	13	Understanding	BTL2
2.	Brief	ly explain about the OSI-reference models	13	Remembering	BTL1
3.	Anal	yse about the network topologies in data communication.	13	Analysing	BTL4
4.	Asse	s the functions of synchronous and asynchronous transmissions.	13	Evaluating	BTL5
5.	(i)	Interpret in detail about the Standards Organizations for Data Communication.	7	Applying	BTL3
	(ii)	Explain the concept of Data communication circuits using a basic block diagram.	6		
6.	(i)	Discuss in detail about the standards organization for Data communication	06	Understanding	BTL2

	(ii)	Describe the following data communication codes: Baudot, ASCII and EBCDIC	07		
7	(;)	Evaluin the working of a simplified two station data	00	Evolucting	DTI 5
/.	(1)	communication circuits Explain the various data transmission	08	Evaluating	DILJ
		modes			
	(ii)	Briefly write on standard organisations for data communication	05		
8	<u>(II)</u> Illus	trate the principles of different types of modems	13	Applying	BTI 3
0.	mus	trace the principles of different types of moderns.	15	rippiying	DILS
9.	(i)	Explain the working of two station data communication circuit with	07	Creating	BTL6
		a block diagram.			
	(ii)	Discuss the various data communication codes and its significance.	06		
10.	(i)	Describe the following data communications code :Baudot, ASCII	06	Remembering	BTL1
		and EBCDIC			
	(ii)	Draw the block diagram of Data Communication system and	07		
		explain	1.0		
11.	Discu	uss the various data communication codes and its significance.	13	Understanding	BTL2
12.	(i)	Describe the two method of error correction in data	07	Applying	BTL3
		communication.			
	(ii)	Demonstrate the working of two station data communication	06		
		circuit with a block diagram			
13.	List	out the various data communication codes that are popularly	13	Analysing	BTL4
	empl	oyed. Also analyse the merits and demerits of them.			
14.	Expl	ain the data communication hardware with neat block diagram and	13	Remembering	BTL1
	expla	in all devices.			
	Infer about the following :			A 1 '	
15.	Infer	about the following :		Analysing	BTL4
15.	Infer (i)	Data communications hardware.	07	Analysing	BTL4
15.	(i) (ii)	Data communications hardware.	07 06	Analysing	BTL4
15. 16.	(i) (ii) Expl	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface.	07 06 13	Understanding	BTL4 BTL2
15. 16. 17.	(i) (ii) Expl	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface.	07 06 13	Understanding Remembering	BTL2 BTL1
15. 16. 17.	(i) (ii) Expl Discu	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example.	07 06 13 13	Understanding Remembering	BTL2 BTL1
15. 16. 17.	Infer (i) (ii) Expl Discu	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C	07 06 13 13	Analysing Understanding Remembering	BTL2 BTL1 BTL5
15. 16. 17.	Infer (i) (ii) Expl Discu Nam (i)	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods	07 06 13 13 15 08	Analysing Understanding Remembering Evaluating Creating	BTL2 BTL1 BTL5 BTL6
15. 16. 17. 1. 2.	Infer (i) (ii) Expl Discu Nam (i) (ii)	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UABT with its line control unit interface diagram	07 06 13 13 13 15 08 07	Analysing Understanding Remembering Evaluating Creating	BTL2 BTL1 BTL5 BTL6
15. 16. 17. 1. 2.	Infer (i) (ii) Expl Discu Nam (i) (ii) Eval	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UART with its line control unit interface diagram. uate the RS-232 interface performance with logic level and poise	07 06 13 13 15 08 07 15	Analysing Understanding Remembering Evaluating Creating Evaluating	BTL2 BTL1 BTL5 BTL6 BTL5
15. 16. 17. 1. 2. 3.	Infer (i) (ii) Expl Discu Nam (i) (ii) Eval marg	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UART with its line control unit interface diagram. uate the RS-232 interface performance with logic level and noise in diagram.	07 06 13 13 15 08 07 15	Analysing Understanding Remembering Evaluating Creating Evaluating	BTL2 BTL1 BTL5 BTL6 BTL5
15. 16. 17. 1. 2. 3. 4.	Infer (i) (ii) Expl Discu Nam (i) (ii) Eval marg Expl	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UART with its line control unit interface diagram. uate the RS-232 interface performance with logic level and noise in diagram. ain the concept of Data communication circuits using a basic block	07 06 13 13 15 08 07 15 15	Analysing Understanding Remembering Evaluating Creating Evaluating	BTL2 BTL1 BTL5 BTL6 BTL5 BTL6
15. 16. 17. 1. 2. 3. 4.	Infer (i) (ii) Expl Discu Nam (i) (ii) Eval marg Expl diagr	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UART with its line control unit interface diagram. uate the RS-232 interface performance with logic level and noise in diagram. ain the concept of Data communication circuits using a basic block ram.	07 06 13 13 15 08 07 15 15	Analysing Understanding Remembering Evaluating Creating Evaluating Creating	BTL2 BTL1 BTL5 BTL6 BTL5 BTL6
15. 16. 17. 1. 2. 3. 4. 5.	Infer (i) (ii) Expl Discu Nam (i) (ii) Eval marg Expl diagr (i)	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UART with its line control unit interface diagram. uate the RS-232 interface performance with logic level and noise in diagram. ain the concept of Data communication circuits using a basic block am. Evaluate the BCS for the following data and CRC-generating	07 06 13 13 15 08 07 15 15 07	Analysing Understanding Remembering Evaluating Creating Creating Creating Evaluating	BTL2 BTL1 BTL5 BTL6 BTL5 BTL6 BTL6 BTL5
15. 16. 17. 1. 2. 3. 4. 5.	Infer (i) (ii) Expl Discu Nam (i) (ii) Eval marg Expl diagr (i)	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UART with its line control unit interface diagram. uate the RS-232 interface performance with logic level and noise in diagram. ain the concept of Data communication circuits using a basic block am. Evaluate the BCS for the following data and CRC-generating polynomials:	07 06 13 13 15 08 07 15 15 07	Analysing Understanding Remembering Evaluating Creating Evaluating Creating Evaluating	BTL4 BTL2 BTL1 BTL5 BTL6 BTL5 BTL6 BTL5 BTL5
15. 16. 17. 1. 2. 3. 4. 5.	Infer (i) (ii) Expl Discu Nam (i) (ii) Eval marg Expl diagr (i)	Data communications hardware.Serial and parallel interfaceain in details the RS-232 interface.uss about the serial and parallel interfaces with suitable example.PART –Ce the types of errors detection methods and explain with examples.Explain elaborately error correction methods.Explain UART with its line control unit interface diagram.uate the RS-232 interface performance with logic level and noise in diagram.ain the concept of Data communication circuits using a basic block am.Evaluate the BCS for the following data and CRC-generating polynomials:Data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$	07 06 13 13 15 08 07 15 15 07	Analysing Understanding Remembering Evaluating Creating Evaluating Creating Evaluating	BTL2 BTL1 BTL5 BTL6 BTL5 BTL6 BTL5
15. 16. 17. 1. 2. 3. 4. 5.	Infer (i) (ii) Expl Discr Nam (i) (ii) Eval marg Expl diagr (i) (ii)	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UART with its line control unit interface diagram. uate the RS-232 interface performance with logic level and noise in diagram. ain the concept of Data communication circuits using a basic block am. Evaluate the BCS for the following data and CRC-generating polynomials: Data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ Draw the block diagram version of the BCS generator for the data	07 06 13 13 15 08 07 15 15 07 07 08	Analysing Understanding Remembering Evaluating Creating Creating Evaluating Evaluating	BTL2 BTL1 BTL5 BTL6 BTL5 BTL6 BTL5
15. 16. 17. 1. 2. 3. 4. 5.	Infer (i) (ii) Expl Discu Nam (i) (ii) Eval marg Expl diagr (i)	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UART with its line control unit interface diagram. uate the RS-232 interface performance with logic level and noise in diagram. ain the concept of Data communication circuits using a basic block am. Evaluate the BCS for the following data and CRC-generating polynomials: Data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ Draw the block diagram version of the BCS generator for the data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ and the CRC generating	07 06 13 13 15 08 07 15 15 07 07 08	Analysing Understanding Remembering Evaluating Creating Evaluating Creating Evaluating	BTL4 BTL2 BTL1 BTL5 BTL6 BTL5 BTL6 BTL5
15. 16. 17. 1. 2. 3. 4. 5.	Infer (i) (ii) Expl Discu (i) (ii) Eval marg Expl diagr (i)	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UART with its line control unit interface diagram. uate the RS-232 interface performance with logic level and noise in diagram. ain the concept of Data communication circuits using a basic block am. Evaluate the BCS for the following data and CRC-generating polynomials: Data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ Draw the block diagram version of the BCS generator for the data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ and the CRC generating polynomials. List the content of the shift registers after every data	07 06 13 13 15 08 07 15 15 07 07 08	Analysing Understanding Remembering Evaluating Creating Creating Evaluating Evaluating	BTL4 BTL2 BTL1 BTL5 BTL6 BTL5 BTL6 BTL5
15. 16. 17. 1. 2. 3. 4. 5.	Infer (i) (ii) Expl Discu Nam (i) (ii) Eval marg Expl diagr (i)	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UART with its line control unit interface diagram. uate the RS-232 interface performance with logic level and noise in diagram. ain the concept of Data communication circuits using a basic block am. Evaluate the BCS for the following data and CRC-generating polynomials: Data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ Draw the block diagram version of the BCS generator for the data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ and the CRC generating polynomials. List the content of the shift registers after every data shift.	07 06 13 13 15 08 07 15 15 07 07 08	Analysing Understanding Remembering Evaluating Creating Evaluating Evaluating	BTL2 BTL1 BTL5 BTL6 BTL5 BTL6 BTL5 BTL6
15. 16. 17. 1. 2. 3. 4. 5.	Infer (i) (ii) Expl Discr (i) Eval marg Expl diagr (i) (ii)	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UART with its line control unit interface diagram. uate the RS-232 interface performance with logic level and noise in diagram. ain the concept of Data communication circuits using a basic block am. Evaluate the BCS for the following data and CRC-generating polynomials: Data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ Draw the block diagram version of the BCS generator for the data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ and the CRC generating polynomials. List the content of the shift registers after every data shift. UNIT -V SOURCE AND ERROR CONTROL C	07 06 13 13 15 08 07 15 15 07 07 08	Analysing Understanding Remembering Evaluating Creating Creating Evaluating Evaluating	BTL2 BTL1 BTL5 BTL6 BTL5 BTL6 BTL5
15. 16. 17. 1. 2. 3. 4. 5. Entro	Infer (i) (ii) Expl Discr Nam (i) (ii) Eval marg Expl diagr (i) (ii)	Data communications hardware. Serial and parallel interface ain in details the RS-232 interface. uss about the serial and parallel interfaces with suitable example. PART –C e the types of errors detection methods and explain with examples. Explain elaborately error correction methods. Explain UART with its line control unit interface diagram. uate the RS-232 interface performance with logic level and noise in diagram. ain the concept of Data communication circuits using a basic block am. Evaluate the BCS for the following data and CRC-generating polynomials: Data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ Draw the block diagram version of the BCS generator for the data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ and the CRC generating polynomials. List the content of the shift registers after every data shift. UNIT -V SOURCE AND ERROR CONTROL C Source encoding theorem - Shannon fano coding - Huffman coding	07 06 13 13 15 08 07 15 15 07 07 08 08	Analysing Understanding Remembering Evaluating Creating Evaluating Evaluating Evaluating	BTL4 BTL2 BTL1 BTL5 BTL6 BTL5 BTL6 BTL5 BTL5 - channel
15. 16. 17. 1. 2. 3. 4. 5. Entro capac	Infer (i) (ii) Expl Discr Nam (i) (ii) Eval marg Expl diagr (i) (ii)	about the following :Data communications hardware.Serial and parallel interfaceain in details the RS-232 interface.uss about the serial and parallel interfaces with suitable example.PART -Ce the types of errors detection methods and explain with examples.Explain elaborately error correction methods.Explain UART with its line control unit interface diagram.uate the RS-232 interface performance with logic level and noise in diagram.ain the concept of Data communication circuits using a basic block ram.Evaluate the BCS for the following data and CRC-generating polynomials:Data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ Draw the block diagram version of the BCS generator for the data $G(x)=x^{10}+x^9+x^7+x^5+x^3+x^2+x^1+x^0$ and the CRC generating polynomials. List the content of the shift registers after every data shift.UNIT -V SOURCE AND ERROR CONTROL CSource encoding theorem - Shannon fano coding - Huffman codingError Control Coding - linear block codes. case study - Cellular Conce	07 06 13 13 15 07 15 15 07 07 08 08 08	Analysing Understanding Remembering Evaluating Creating Evaluating Creating Evaluating VG utual information G,2G,3G,4G	BTL2 BTL2 BTL1 BTL5 BTL6 BTL5 BTL6 BTL5 - channel

Q. No	Questions	Competence	BT Level
1.	Define Entropy.	Remembering	BTL1
2.	Write down the expression for entropy	Understanding	BTL2
3.	An event has six possible outcomes with probabilities 1/2, 1/4, 1/8, 1/16, 1/32,	Evaluating	BTL5
	1/32. Find the entropy of the system		
4.	Find the entropy of the source $X = \{x_1, x_2\}$ with the equal message probabilities.	Creating	BTL6
5.	Point out the properties of mutual information.	Analysing	BTL4
6.	Define channel capacity of a discrete memory less channel	Remembering	BTL1
7.	Interpret about FEC	Applying	BTL3
8.	What is error control code?	Understanding	BTL2
9.	Demonstrate about linear block codes?	Applying	BTL3
10.	Conclude that if C_i , and C_j are two code vectors in the (n,k) linear block code,	Evaluating	BTL5
	then their sum is also a code vector with an example.		
11.	Show that $C = \{000,001,101\}$ is not a linear code.	Applying	BTL3
12.	Give the error correcting capability of a linear block code.	Remembering	BTL1
13.	When a binary code is said to be cyclic code?	Analysing	BTL4
14.	Point out the properties of cyclic codes.	Analysing	BTL4
15.	What is prefix coding?	Understanding	BTL2
16.	Illustrate about code redundancy	Applying	BTL3
17.	How can the information capacity of the channel can be improved?	Analysing	BTL4
18.	What is meant by code variance	Understanding	BTL2
19.	Define syndrome	Remembering	BTL1
20.	What is Hamming code?	Understanding	BTL2
21.	Evaluate the hamming weight of 10110 and the hamming distance between	Evaluating	BTL5
	1010 and 0000.		
22.	Check and modify LBC $(6,3)$ for hamming, when d_{min} is 4.	Creating	BTL6
23.	What are the various handovers carried out in GSM?	Remembering	BTL1
24.	List the advantage of cell splitting concept?	Remembering	BTL1
PART –B			рт
Q. No	Questions	Competence	Level
1.	(i) State the Source coding theorem and explain. 06	Remembering	BTL1
	(ii) Describe the properties of mutual information 07 A Source concretes five measures in the properties of mutual information 04	Anolysiaa	
2.	A source generates rive messages m_0 , m_1 , m_2 , m_3 , m_4 with probabilities 04 0.55, 0.15, 0.15, 0.10 and 0.05 respectively. The successive messages 04	Anaiysing	DIL4

	emitted by the source are statistically independent. Determine code words	05		
	for the message using Shannon Fano Algorithm and Huffman. Compare			
	their efficiency.			
3.	Five source message are probable to appear as $m_1 = 0.4$, $m_2 = 0.15$, $m_3 =$	06	Evaluating	BTL5
	0.15. $m_4=0.15$ and $m_5=0.15$. Evaluate coding efficiency by using the	07	8	
	following algorithms (i) Shannon - Fano (ii) Huffman	0,		
4	Encode the following messages with their respective probability using	06	Applying	BTI 3
	basic Huffman algorithm	07	rippiying	DILJ
	M_1 M_2 M_2 M_4 M_5 M_6 M_7 M_0	07		
	1/2 $1/2$ $1/2$ $1/16$ $1/16$ $1/16$ $1/22$ $1/22$			
	1/2 1/8 1/8 1/10 1/10 1/10 1/52 1/52			
	(i) Circulate the Haffman and a fam a diameter memory has a mith	10	TT. d	
5.	(1) Give the Huffman code for a discrete memory less source with	10	Understanding	BIL2
	probability statistics (0.1, 0.1, 0.2, 0.2, 0.4}			
	(ii) Describe the concepts of channel capacity	03		
6.	State the relationship between mutual information and channel capacity	13	Remembering	BILI
	and also derive the expression for mutual information.			
7.	The generator polynomial of (7,4) cyclic code is given by	13	Applying	BTL3
	$G(D)=1+D+D^2$. Compute all the non-systematic code words with			
	necessary steps.			
8.	Analyse and construct an Encoder for (7,4) Linear block code and also	13	Analysing	BTL4
	construct a suitable syndrome decoder for the same and explain.			
9.	For a (6,3) systematic linear block code, the three parity-check bits c4,c5		Evaluating	BTL5
	and c_6 are formed from the following equations: $c_4=d_1+d_3$, $c_5=d_1+d_2+d_3$,			
	$c_6=d_1+d_2$			
	(i) Write down the generator matrix.			
	(ii) Construct all possible generator matrix.	03		
	(iii) Suppose that the received word is 01011. Decode this received	06		
	word by finding the location of the error and the transmitted			
10	data bits.	07	TT 1 / 1'	
10.	(i) Write short notes on Linear block code.	0/	Understanding	BTL2
	(ii) Summarize the detection of error with the help of syndrome and vector error	00		
11	Consider a systematic block code whose parity check equation are	06	Analysing	BTI 4
	consider a systematic block code whose party check equation are	00	7 mary sing	DILI
	$P_1 - m_1 + m_2 + m_4$; $P_2 - m_1 + m_2 + m_4$; $P_2 - m_1 + m_2 + m_2$; $P_4 - m_2 + m_4$ Where m:	02		
	$P_1=m_1+m_2+m_4$; $P_2=m_1+m_3+m_4$; $P_3=m_1+m_2+m_3$; $P_4=m_2+m_3+m_4$. Where m_i is the message digits and P_i are the parity digits	02		
	$P_1=m_1+m_2+m_4$; $P_2=m_1+m_3+m_4$; $P_3=m_1+m_2+m_3$; $P_4=m_2+m_3+m_4$. Where m_i is the message digits and P_i are the parity digits.	02 05		
	 P1=m1+m2+m4; P2=m1+m3+m4; P3=m1+m2+m3; P4=m2+m3+m4. Where mi is the message digits and Pi are the parity digits. (i) Find the generator matrix and the parity check matrix for this code 	02 05		
	 P1=m1+m2+m4; P2=m1+m3+m4; P3=m1+m2+m3; P4=m2+m3+m4. Where mi is the message digits and Pi are the parity digits. (i) Find the generator matrix and the parity check matrix for this code (ii) How many errors can be detected and corrected? 	02 05		
	 P1=m1+m2+m4; P2=m1+m3+m4; P3=m1+m2+m3; P4=m2+m3+m4. Where mi is the message digits and Pi are the parity digits. (i) Find the generator matrix and the parity check matrix for this code (ii) How many errors can be detected and corrected? (iii) If the received code word is 10101010, find the syndrome. 	02 05		
12.	 P1=m1+m2+m4; P2=m1+m3+m4; P3=m1+m2+m3; P4=m2+m3+m4. Where mi is the message digits and Pi are the parity digits. (i) Find the generator matrix and the parity check matrix for this code (ii) How many errors can be detected and corrected? (iii) If the received code word is 10101010, find the syndrome. The generator matrix for a (6,3) block code is given below. Calculate all	02 05 13	Applying	BTL3
12.	 P1=m1+m2+m4; P2=m1+m3+m4; P3=m1+m2+m3; P4=m2+m3+m4. Where mi is the message digits and Pi are the parity digits. (i) Find the generator matrix and the parity check matrix for this code (ii) How many errors can be detected and corrected? (iii) If the received code word is 10101010, find the syndrome. The generator matrix for a (6,3) block code is given below. Calculate all possible the code words	02 05 13	Applying	BTL3
12.	 P1=m1+m2+m4; P2=m1+m3+m4; P3=m1+m2+m3; P4=m2+m3+m4. Where mi is the message digits and Pi are the parity digits. (i) Find the generator matrix and the parity check matrix for this code (ii) How many errors can be detected and corrected? (iii) If the received code word is 10101010, find the syndrome. The generator matrix for a (6,3) block code is given below. Calculate all possible the code words 1 0 0 1 1 0 	02 05 13	Applying	BTL3
12.	$P_{1}=m_{1}+m_{2}+m_{4}; P_{2}=m_{1}+m_{3}+m_{4}; P_{3}=m_{1}+m_{2}+m_{3}; P_{4}=m_{2}+m_{3}+m_{4}.$ Where mi is the message digits and Pi are the parity digits. (i) Find the generator matrix and the parity check matrix for this code (ii) How many errors can be detected and corrected? (iii) If the received code word is 10101010, find the syndrome. The generator matrix for a (6,3) block code is given below. Calculate all possible the code words $1 \ 0 \ 0 \ 1 \ 1 \ 0$ $G = 0 \ 1 \ 0 \ 1 \ 0 \ 1$	02 05 13	Applying	BTL3

13.	(i)	Design a syndrome calculator for (7,4) cyclic code generated by the	07	Creating	BTL6
		polynomial $g(x)=x^3+x+1$. Calculate the syndrome for received			
	(ii)	Design a cyclic encoder for the same (7.4) cyclic code and obtain	06		
	(11)	code vector for the message vector 1100 .	00		
14.	Expl	ain the concept of code generation and decoding of convolutional	13	Remembering	BTL1
15.	Writ	e short notes on Cellular Concept-1G,2G,3G,4G.	13	Understanding	BTL2
16.	Desc	ribe the architecture of GSM with a neat diagram.	13	Remembering	BTL1
17.	Expl exam	ain the concept of cellular topology and cell fundamentals with ples.	13	Understanding	BTL2
		PART –C			DT
Q. No	Ques	tions		Competence	Level
1.	The s	source of information A generates the symbols { A_0 , A_1 , A_2 , A_3 and with the corresponding probabilities $(0, 4, 0, 2, 0, 15, 0, 1, 20, 0, 5)$	15	Evaluating	BTL5
	Enco	de the source symbols using binary encoder and Shannon-Fano			
2	enco	der and compare its efficiency.	07	Evolucting	DTI 5
2.	(1)	the Signal to Noise Ratio (SNR) on the channel is 30 dB, determine	07	Evaluating	DILJ
		the capacity of the channel. If the above channel is to be used to			
		transmit 4.8 kbps of data determine the minimum SNR required on the channel			
	(ii)	A black and white TV picture consists of about 2×10^6 picture	08		
		elements with 16 different brightness levels, with equal			
		calculate the average rate of information conveyed by this TV			
		picture source. If SNR is 30 dB, what is the maximum bandwidth			
2	Cons	required to support the transmission of the resultant video signal.	15	Creating	
5.	x^3+x^2	2 +1. Determine the following. 1). Generator Matrix 2). Parity Check	15	Creating	DILO
	Matr	3). Decoding table 4). Verify the received vector 1101101 for			
4	error	and correct it, if any error.	15	Evaluating	BTI 5
	ine j	arry check matrix of a particular (7,4) micar block code is given by	15	Lvaluating	DILJ
		$[H] = \begin{bmatrix} 1 & 1 & 1 & 0 & 1 & 0 & 0 \\ 1 & 1 & 0 & 1 & 0 & 1 & 0 \end{bmatrix}$			
		$\begin{bmatrix} II \end{bmatrix} = \begin{bmatrix} I & I & 0 & I & 0 & I & 0 \\ I & 0 & I & 1 & 0 & 0 & 1 \end{bmatrix}$			
	C	Find the conceptor metric C			
	(1 (1	i) List all the code vectors.			
	(i	ii) What is the minimum distance between code vectors?			
	(i	v) How many errors can be detected? How many errors can be corrected?			
5.	The g	generation polynomial of a (15,11) Hamming code is defined by $g(x) =$	15	Creating	BTL6
	1+x-	$+x^4$. Develop the encoder and syndrome calculator for this code,			
	usin mese	g a systematic form for the code. Generate the code word for the sage vector (1111, 1111, 111) using the developed encoder. Find the			
	outp	ut of the designed syndrome calculator for the received code word			
	(111	1 1111 1111 111).			

